

ABSTRACT

A mechanism for providing zero power control of a computer peripheral is disclosed. The mechanism comprises a switch electrically connected to a card detecting pin of the host device. The switch is operated by a retractable antenna of the card. In this respect, when the antenna is in a retracted position, the switch generates a "removed" signal to the card detecting pin. The "removed" signal simulates that the computer peripheral has been removed from the host device such that the operating software of the host device will not supply power to the card. On the other hand, when the switch detects the antenna in the extended state, the switch will generate an "inserted" signal to the card detecting pin. The "inserted" signal informs the host device operating system that a card has been inserted and that power should be applied to the card.